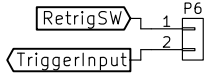
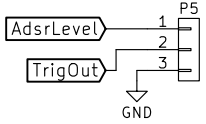
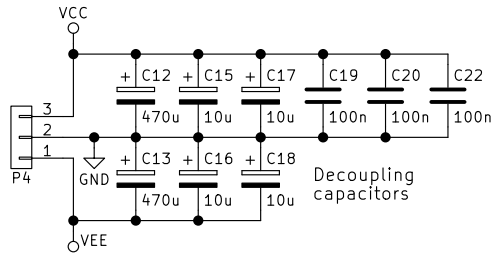


Note 1
Ensure 5V for VCC for these, or adjust 220R resistors accordingly

Note 2
For PSU > +5V, the cheaper TL074 can be used instead (same pinout)



"SET" on flipflop Spike caused by upward flank on gate/trigger

"RESET" on flipflop Spike caused by attack cycle comparator; ends attack cycle

MAX level reached comparator
MAX ADSR Level buffer
Set to desired max output voltage, 10V suggested

Sustain Lvl buffer

ADSR out buffer

To SPCO SW GND